

**AMENDMENTS TO THE SPECIFICATION**

Amendments to the paragraph beginning on page 2, line 2 of the filed specification are as follows:

This application is related to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,057 [59182 P001US 10020638], filed October 31, 2000, entitled "System And Method For IP Router With an Optical Core," to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,056 [59182 P002US 10020639], filed October 31, 2000, entitled "System and Method for Router Central Arbitration," to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/702,958 [59182 P006US 10020643], filed October 31, 2000, entitled "Timing and Synchronization for an IP Router Using an Optical Switch," to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,027 [59182 P012US 10021641], filed October 31, 2000, entitled "Router Network Protection Using Multiple Facility Interfaces," to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,043 [59182 P013US 10021642], filed October 31, 2000, entitled "Router Line Card Protection Using One-for-N Redundancy" and to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,064 [59182 P014US 10021643], filed October 31, 2000, entitled "Router Switch Fabric Protection Using Forward Error Correction," the disclosures of which are incorporated herein by reference.

Amendments to the paragraph beginning on page 4, line 23 of the filed specification are as follows:

Various aspects of the invention are described in concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,057 [59182 P001US 10020638], filed October 31, 2000, entitled "System And Method For IP Router With an Optical Core," to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,056 [59182 P002US 10020639], filed October 31, 2000, entitled "System and Method for Router Central Arbitration," to concurrently filed, co-pending, and

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commonly assigned U.S. Application Serial Number 09/702,958 [~~59182 P006US 10020643~~], filed October 31, 2000, entitled "Timing and Synchronization for an IP Router Using an Optical Switch," to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,027 [~~59182 P012US 10021641~~], filed October 31, 2000, entitled "Router Network Protection Using Multiple Facility Interfaces," to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,043 [~~59182 P013US 10021642~~], filed October 31, 2000, entitled "Router Line Card Protection Using One-for-N Redundancy" and to concurrently filed, co-pending, and commonly assigned U.S. Application Serial Number 09/703,064 [~~59182 P014US 10021643~~], filed October 31, 2000, entitled "Router Switch Fabric Protection Using Forward Error Correction," the disclosures of which are incorporated herein by reference.